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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/596,226

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Roberto Magri

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EXAMINER

LIU, LI

ART UNIT

PAPER NUMBER

2613

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DELIVERY MODE

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PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/596,226	Applicant(s) MAGRI ET AL.	
	Examiner LI LIU	Art Unit 2613	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 June 2006 and 04 February 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 10-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 10-12 and 15-20 is/are rejected.
- 7) ☒ Claim(s) 13 and 14 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 June 2006 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>6/21/2006</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement (IDS) submitted on 6/21/2006 is being considered by the examiner.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 10 and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Salehee (Salehee: "Closed Cycle Lasing of ASE Noise in a WDM Ring Network", CLEO/Pacific Rim 2001, 15-19 July 2001, Vol. 2, pages II-558-559) in view of Caprino et al (WO 02/080409).

1). With regard to claim 10, Salehee discloses a wavelength division multiplex optical ring network (Figure 1) comprising :

optical fiber arranged in a ring configuration (Figure 1);

a plurality of doped fiber optical amplifiers (Figure 1, the EDFA) arranged in the ring, wherein a spectral response in the ring is configured such that amplified spontaneous emission (ASE) noise circulating around the ring in a lasing mode (e.g., the channel 4 acts as the lasing channel/mode) is used to clamp a gain of each doped fiber optical amplifier (Figures 3 and 4, the gain of each amplifier is clamped).

But, in Figure 1 etc, Salehee does not expressly show that a controller associated with each optical amplifier to control the optical amplifier to produce a substantially constant output power or to maintain a substantially constant pump power.

However, as shown in Figures 3 and 4, by using the VOA and lasing channel, the output power of the amplifier is substantially constant. Another prior art, Capriono et al, discloses that the system (controller and method) to control the optical amplifier to produce a substantially constant output power or to maintain a substantially constant pump power is known in the art (page 5 line 20-29 and page 7 line 9-16, “[t]he site optical amplifiers are allowed to work with constant output power settable by means of known electronic control loops. The laser pumps are also controlled in a known manner in order to hold constant the output power without regard for the input power”).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to apply the controller as known in the art to the system of Salehee so that a constant output power from the amplifier can be obtained, and the system reliability can be enhanced, and the transient effect can be controlled.

2). With regard to claim 20, Salehee and Capriono et al disclose all of the subject matter as applied to claim 10 above. And the combination of Salehee and Capriono et al further discloses wherein a working point of the optical amplifiers is changed while in use to restore a level of the ASE peak in the event of a slow drift of the optical amplifiers (Figures 3 and 4 of Salehee, and Capriono: page 5 line 20-29 and page 7 line 9-16, “the amplifier be controlled so as to have constant output power without concern for the input power, equation (5) is automatically verified because of the amplifier control loop”).

4. Claims 11 and 12 are rejected under 35 U.S.C. 103(a) as being unpatentable over Salehee and Caprino et al as applied to claim 10 above, and in further view of Stentz et al (US 7,019,894).

1). With regard to claim 11, Salehee and Capriono et al disclose all of the subject matter as applied to claim 10 above. And the combination of Salehee and Capriono et al further discloses detector circuitry configured to switch control of the optical amplifiers to a different mode of operation responsive to detecting an absence of an input signal (Capriono: page 5 line 20-29 and page 7 line 9-16, the control of the optical amplifiers is switched from a normal operation mode "power control mode" to a "gain-control mode" when the amplifier receives no input. It is inherent that a detector circuitry is in Capriono's system so that it can detect whether an input signal is received).

But, Capriono et al does not expressly state to detect an absence of a lasing peak. However, Stentz et al teaches an amplifier having automatic gain control using the ASE as the monitor parameter (Figures 3-8, and as shown in Figures 2,4 and 6 etc, a detector circuitry is used to detect the absence of the ASE signal). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use the ASE lasing peak as the monitoring signal as taught by Stentz et al to the system of Salehee and Capriono et al so that the system can determine the line condition more accurately, and the system performance can be enhanced.

2). With regard to claim 12, Salehee and Capriono et al and Stentz et al disclose all of the subject matter as applied to claims 10 and 11 above. And the combination of

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Salehee and Capriono et al and Stentz et al further discloses wherein the detector circuitry is further configured to switch the optical amplifiers to a gain control mode after detecting a loss of the lasing peak to maintain a gain at substantially a level provided by the optical amplifiers prior to the detected loss (Capriono: page 5 line 20-29 and page 7 line 9-16, the optical amplifiers is switched from a normal operation mode "power control mode" to a "gain-control mode" when the amplifier receives no input to maintain a gain at substantially a level provided by the optical amplifiers prior to the detected loss, or "when there is no input signal the amplifier increases its power gain to amplify the internal noise until it takes its own output power to the value which it was predetermined that it should keep").

5. Claims 15-19 are rejected under 35 U.S.C. 103(a) as being unpatentable over Salehee and Caprino et al and Stentz et al as applied to claims 10 and 11 above, and in further view of Roberts (US 5,969,840).

1). With regard to claim 15, Salehee and Capriono et al and Stentz et al disclose all of the subject matter as applied to claims 10 and 11 above. And the combination of Salehee and Capriono et al and Stentz et al further disclose wherein the detector circuitry further comprises:

a splitter (e.g., 70 in Figure 6 of Stentz) configured to tap a fraction of each optical amplifier's input power; and

a photodiode (72 in Figure 6 of Stentz) configured to measure the input power.

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But, Salehee and Capriono et al and Stentz et al do not expressly disclose a plurality of splitters and a plurality of photodiodes configured to measure the input power.

However, to use a plurality of splitters and photodiodes to measure the input power is known in the art. Roberts teaches a system and method to monitor the input power of an optical amplifier (Figure 1, the element 3 can be an optical amplifier, column 5 line 4) in which a plurality of splitters (e.g., one splitter in front the amplifier 3, and other splitters are in the Control System 5 as shown in Figure 2) and a plurality of photodiodes (Figure 2, the PIN diode in device 6, column 5 line 21-26) configured to measure the input power.

Capriono et al teaches to measure the total input power, Stentz et al teaches to monitor specific ASE band, and Roberts teaches to use a plurality photodiodes to monitor different bands. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to use multiple splitters and photodiodes as taught by Roberts to the system of Salehee and Capriono et al and Stentz et al so that both the total input power and ASE lasing peak can be monitored at the input of the amplifier, and the system monitoring and controlling can be more accurate and reliable.

2). With regard to claim 16, Salehee and Capriono et al and Stentz et al and Roberts disclose all of the subject matter as applied to claims 10, 11 and 15 above. And the combination of Salehee and Capriono et al and Stentz et al and Roberts further discloses wherein the plurality of splitters are further configured to tap a fraction of each optical amplifier's output power (e.g., Stentz: tap 64 in Figure 6), and wherein the

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plurality of photodiodes are further configured to measure the output power (e.g., Stentz: photodiode 66. Or Figure 7 of Roberts).

3). With regard to claim 17, Salehee and Capriono et al and Stentz et al and Roberts disclose all of the subject matter as applied to claims 10, 11 and 15 above. And the combination of Salehee and Capriono et al and Stentz et al and Roberts further discloses wherein the detector circuitry further comprises a filter to pass only ASE noise, and a peak detector to detect the presence or absence of the lasing peak (Stentz teaches to monitor specific ASE band, and Roberts teaches that a particular wavelength band is monitored, therefore it is inherent a filter is used in the system of Roberts so that the particular wavelength band can be detected. That is, the combination of Salehee and Capriono et al and Stentz et al and Roberts a filter to pass only ASE noise, and a peak detector to detect the presence or absence of the lasing peak).

4). With regard to claim 18, Salehee and Capriono et al and Stentz et al and Roberts disclose all of the subject matter as applied to claims 10, 11 and 15 above. And the combination of Salehee and Capriono et al and Stentz et al and Roberts further discloses wherein the detector circuitry further comprises a filter to pass only ASE noise, and control logic to detect a simultaneous decrease in the powers of both the ASE noise peak and the total power input (Stentz teaches to monitor specific ASE band, and Roberts teaches that a particular wavelength band is monitored, therefore it is inherent a filter is used in the system of Roberts so that the particular wavelength band can be detected, and Roberts also teaches a control logic, e.g., devices 7/8 in Figure 2, which detects/determines the powers of each band; and the combination of Salehee

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and Capriono et al and Stentz et al teaches to monitor the ASE noise. That is, the combination of Salehee and Capriono et al and Stentz et al and Roberts teaches/suggests a filter to pass only ASE noise, and a control logic to detect a simultaneous decrease in the powers of both the ASE noise peak and the total power input).

5). With regard to claim 19, Salehee and Capriono et al and Stentz et al and Roberts disclose all of the subject matter as applied to claims 10, 11 and 15 above. And the combination of Salehee and Capriono et al and Stentz et al and Roberts further discloses wherein the detector circuitry further comprises a detector to detect a decrease in the input power to each optical amplifier (Capriono teaches a detecting circuitry that detects whether an input signal is received. And Stentz et al and Roberts teach to monitor the input power. That is, the combination of Salehee and Capriono et al and Stentz et al and Roberts teaches/suggests wherein the detector circuitry comprises a detector to detect a decrease in the input power to each optical amplifier).

Allowable Subject Matter

6. Claims 13 and 14 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to LI LIU whose telephone number is (571)270-1084. The examiner can normally be reached on Monday-Friday, 8:30 am - 6:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ken Vanderpuye can be reached on (571)272-3078. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Li Liu/
Primary Examiner, Art Unit 2613
September 11, 2010